IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Applicant:

James Joseph Chambers

Filed:

Herewith

For:

MULTIPLE-GATE MOSFET DEVICE WITH LITHOGRAPHY INDEPENDENT SILICON BODY THICKNESS AND METHODS FOR FABRICATING THE SAME

INFORMATION DISCLOSURE STATEMENT

1. Pursuant to 37 C.F.R. 1.97 and 1.98, and in compliance with 37 C.F.R. 1.56, the Office's attention is directed to the patents, pending applications, publications and other information listed on the attached PTO-

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

| previously | copy of each listed document is enclosed except for: (a) pending applications or (b) those cited or submitted to the Office in the following application(s) upon which this application relies for illing date under 35 U.S.C. 120: |
|------------------------|---|
| Serial N Filing D | lo.: ate: |
| PTO-1449 treated ac | any document, publication or other information for which a date is not given on the attached, Applicant(s) believe(s) the same may qualify as "prior" art to this application and should be cordingly, although Applicant(s) reserve(s) the right to contest the prior art status of any publication or information, should issue arise. |
| accompan | arding each listed document that is not in the English language, an English-language translation ies this Statement as indicated on the attached PTO-1449 or a concise explanation of the of the document is set forth in the following document(s): |
| (a) _ | Copy of each English language version of a search report indicating the degree of relevance found by the foreign office of each document being submitted from the search report. |
| (b) _ | Attachment entitled "Concise Explanation of Relevance of Non-English Language Documents". |
| 3. Purs | uant to 37 C.F.R. 1.97(b) this Statement is being filed (one must be checked): |
| (a) <u>X</u> | Within 3 months of the filing date or date of entry into the National Stage. |
| (b) | Before the mailing date of a first Office Action on the merits. If this Statement is not filed before the mailing date of a first Office Action on the merits, the required certification is given below or, in the absence thereof, the Office is authorized to charge the required fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668 for consideration of this Statement. |
| (c) | Refore the mailing date of a first Office Action after the filing of a request for continued |

examination under 37 C.F.R. 1.114.

| (d) | After the period set forth in 37 C.F.R. 1.97(b) but before the mailing date of either a final action or a notice of allowance. |
|---------------|---|
| (1) _ | The required certification is given below, or |
| (2) _ | Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(p) for consideration of this Statement, or |
| (3) _ | _ Charge the fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668 |
| (e) | After the mailing date of either a final action or a notice of allowance, but before payment of the issue fee. Petition hereby is made for consideration of this Statement and the required certification is indicated below. |
| (1)_ | _ Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(i)(1), or |
| (2)_ | Charge the fee set forth in 37 C.F.R. 1.17(i)(1) to Deposit Account No. 20-0668. |
| 4. Certifica | ation (if applicable) |
| (a) | The undersigned hereby certifies that each item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this Statement. |
| (b) | The undersigned hereby certifies that no item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the undersigned's knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than 3 months prior to the filing of this Statement. |
| | mmissioner is hereby authorized to charge any additional fees or credit any overpayment to |
| Deposit Accou | unt No. 20-0668. Respectfully Submitted, |
| | ESCHWEILER & ASSOCIATES, LLC |
| | |

Thomas G. Eschweiler Registration No. 36,981

National City Bank Building 629 Euclid Avenue, Suite 1210 Cleveland, Ohio 44114 (216) 502-0600

CERTIFICATION UNDER 37 CFR 1.10

I hereby certify that this paper, and the documents referred to as attached or enclosed, are being deposited with the United States Postal Service on the date set forth below in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR 1.10, with the below indicated mailing label number, addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: October 29, 2003

Christine Gillray
Christine Gillroy

Mailing Label Number <u>EV317555458US</u>

T

Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

| | Under the Paperwork | Reduction Act of 1995, no per | sons are req | uired to respond to a collection | n of information unless it displays a valid OMB control number. | | |
|-----------------------------------|---------------------|-------------------------------|--------------|----------------------------------|---|--|--|
| | | | | Complete If Known | | | |
| | Substitute for | Form 1449A/PTO | | Application Number | | | |
| | INFORMATIO | N DISCLOSURE | | Filing Date | | | |
| STATEMENT BY APPLICANT | | | | First Named Inventor | Chambers et al. | | |
| | luco ac many c | hoots as noossand | | Group Art Unit | | | |
| (use as many sneets as necessary) | | | | Examiner Name | | | |
| Sheet | 1 | of | 1 | Attorney Docket No. | TI-36030 | | |
| Sheet | | heets as necessary) | 1 | Group Art Unit Examiner Name | | | |

| | | | | U.S. PATENT DOC | UMENTS | |
|--------------------|--------------|----------------------|-----------------------------------|-------------------------|-----------------------------|--|
| | | U.S. Patent Document | | Name of Patentee or | Date of Pub. of | |
| Exam. Initials* | Cite No.1 | Number | Kind Code ² (if known) | Applicant of Cited Doc. | Cited Doc. (mm- dd-yyyy) | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | AA | 5,804,848 | | Mukai | 09/08/1998 | Entire Document |
| | AB | 5,899,710 | | Mukai | 05/04/1999 | Entire Document |
| | AC | 2003/0136963 | A1 | Krivokapic et al. | 07/24/2003 | Entire Document |
| | AD | | | | | |
| | AE | | | | | |

| | | | | | FOREIGN PATE | NT DOCUMENTS | | |
|--------------------|--------------|---------------------|------------------------------|---|---|---|--|------------|
| Exam. Initials* | Cite No.1 | Office ³ | oreign Patent Doc Number⁴ | ument Kind Code ² (if known) | Name of Patentee or Applicant of Cited Doc. | Date of Pub. of Cited Doc. (mm-dd-yyyy) | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | ⊤ ⁵ |
| | ВА | | | | | | | · |
| | ВВ | | | | | | | |

| | _ | OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS | |
|--------------------|--------------|--|----|
| Exam. Initials* | Cite No.1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T² |
| | CA | "25nm CMOS Omega FETs", IEEE, FU-LIANG YANG, HAO-YU CHEN, FANG-CHENG CHEN, CHANG-CHUAN HUANG, CHANG-YUN CHANG, HSIEN-KRANG CHIU, CHI-CHUANG LEE, CHI-CHUN CHEN, HUAN-TSUANG HUANG, CHIH-JIAN CHEN HUN-JAN TAO, YEE-CHIA YEO, MONG-SONG LIANG and CHENMING HU, 2002, 4 pgs. | |
| | СВ | "SOI Devices for Sub-0.1 μm Gate Lengths", PROC. 23 rd International Conference on Microelectronics (Miel 2002), Vol 1, NIS, Yugoslavia, May 12-15, 2002, J.P. COLINGE, J.T. PARK and C.A. COLINGE, pp. 109-113. | |
| | сс | "Advanced Depleted-Substrate Transistors: Single-Gate, Double-Gate and Tri-Gate", 2002 International Conference on Solid State Devices and Materials (SSDM 2002, Nagoya, Japan, ROBERT CHAU, BRIAN DOYLE, JACK KAVALIEROS, DOUGLAS BARLAGE, ANAND MURTHY, MARK DOCZY, RAFAIL RIOS, TOM LINTON, REZA ARGHAVANI, BEN JIN, SUMAN DATTA and SCOTT HARELAND, pp.1-23. | |
| | CD | "Beyond the Conventional Transistor", IBM J. Res. & Diev., Vol. 46, No. 2/3, March/May 2002, HS.P. WONG, PP. 133-168. | |
| | CE | | |
| | CF | | |
| | CG | | |
| | СН | | |

| Examiner | Date | _ |
|-----------|------------|---|
| Signature | Considered | |

US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Other Prior Art/Non-Patent Literature Documents: ¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.